Attorney Docket No.: 42390P13560

**Patent** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application for:

Thomas E. Pearson et al.

Serial No.: 10/080,438

Filing Date: February 21, 2002

For: INTERPOSER TO COUPLE A
MICROELECTRONIC DEVICE
PACKAGE TO A CIRCUIT BOARD

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Examiner: Vigushin, John B.

Group Art Unit: 2827

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

December 2, 2003

(Date of Deposit)

Julie Arango

(Printed name)

(Signature) (I

## **REQUEST FOR STATUS**

Sir:

Applicant respectfully requests the current status for the above-referenced patent application. Applicant timely filed an Amendment and Response to Office Action on July 7, 2003.

In support of this Request for Status, attached please find a copy of the stamped returned postcard dated July 7, 2003.

In view of the information presented, Applicant hereby respectfully requests the current status for the above-referenced application.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 12/2/03

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## BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP LOS ANGELES

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Application No.: 10/080,438 Filing Date: 2/21/2002 Docket #: 042390.P13560

Date Mailed: 07/01/2003 Due Date(s): 07/09/2003

Client: INTEL CORPORATION Atty/Sec: JMB/jaa

Title: Interposer to Couple a Microelectronic Device Package to a Circuit Board

First Named Inventor: Thomas E. Pearson

The following has been received in the U.S.P.T.O. on the date stamped hereon:

Amendment: Response (12 pgs)
Certificate of Mailing

Fee Transmittal (original & copy)

Request for Approval of Drawing Change (1 pg.), Fig. 1 showing proposed amendment (1 pg.) and

formal amended Fig. 1 (1 pg.)

**Postcard** 

Assignee: Intel Corporation



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